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INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANT(S) Mark A. CHAPMAN et al.			
				FILING DATE		GROUP 2863	

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
TL		5,719,789	02/1998	KAWAMATA, Naoki	364	561
tl		5,512,974	04/1996	ABE et al.	354	430
tl		4,338,672	07/1982	PERZLEY et al.	364	513

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
tl		EP 0 704 678 A1	04-03-1996	EUROPE		
tl		0753804	01/15/97	Europe		
tl		2296766	07/10/96	United Kingdom		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
tl	'Digital Signal Processing Applications Using the ADSP-2100 Family, Volume 2' by The Application Engineering Staff of Analogue Devices DSP Division; Edited by Jere Babst; PTR Prentice Hall 1995; IBSN 0-13-178567-2; Pages 46-55
tl	'Digital Signal Processing Applications with Motorola's DSP5602 Processor' by Mohamed El-Sharkawy; Prentice Hall PTR 1996; IBSN 0-13-569746-0; Pages 157-165

EXAMINER TJL	DATE CONSIDERED 4-12-05
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Date: June 30, 2003